

On Parallelizing the MRRR Algorithm for Data-Parallel Coprocessors.

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Symmetric Eigenproblem

- Matrix form:

$$\mathbf{TU} = \mathbf{U}\Lambda$$

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$$\mathbf{T}\mathbf{U} = \mathbf{U}\mathbf{\Lambda}$$

- Vector form:

$$\mathbf{T}\mathbf{u}_i = \lambda_i \mathbf{u}_i$$

Numerical Symmetric Eigenproblem

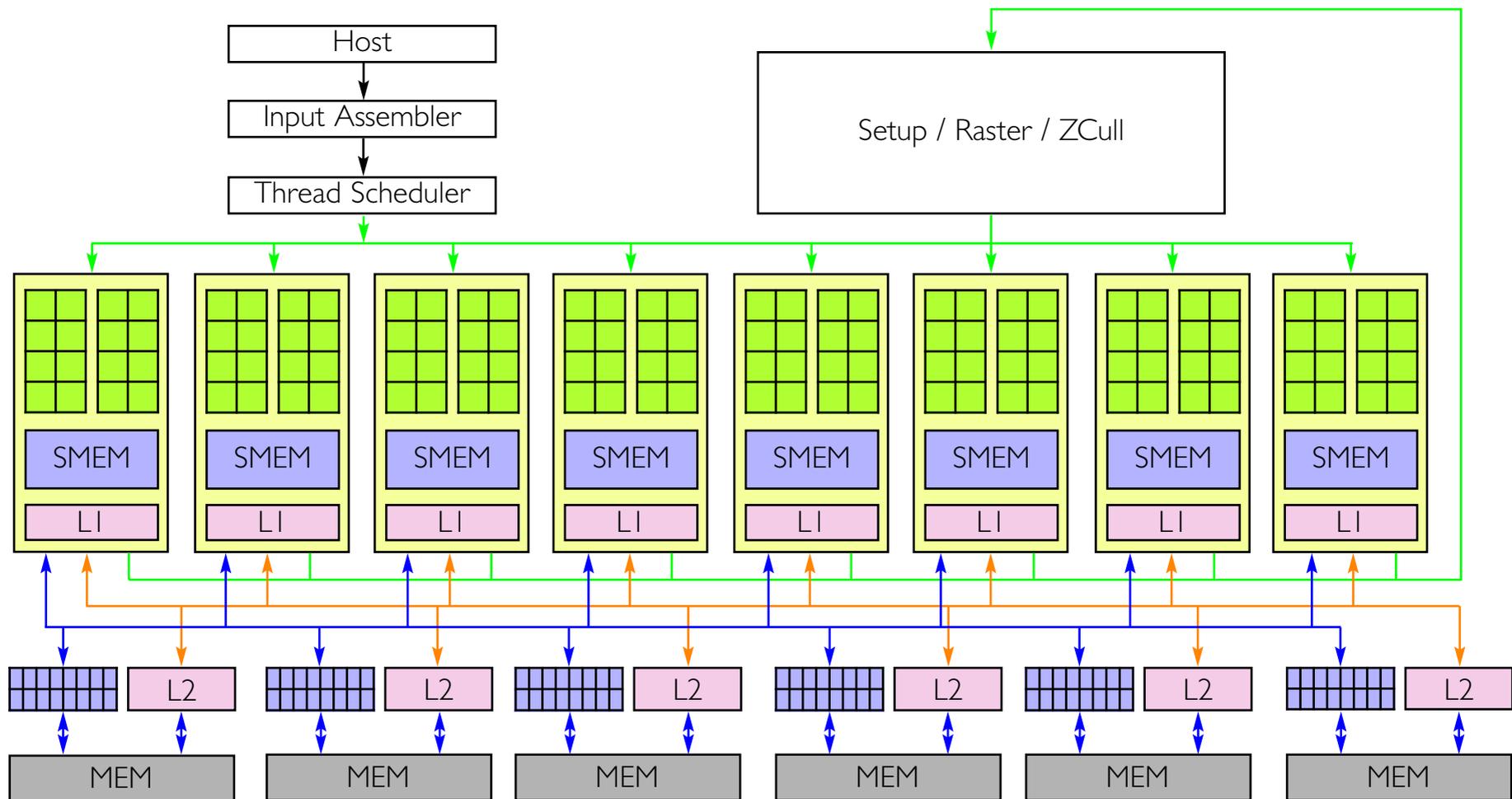
- Small residual:

$$\|\mathbf{T}\tilde{\mathbf{U}} - \tilde{\mathbf{U}}\tilde{\Lambda}\| = \mathcal{O}(n \epsilon \|\mathbf{T}\|)$$

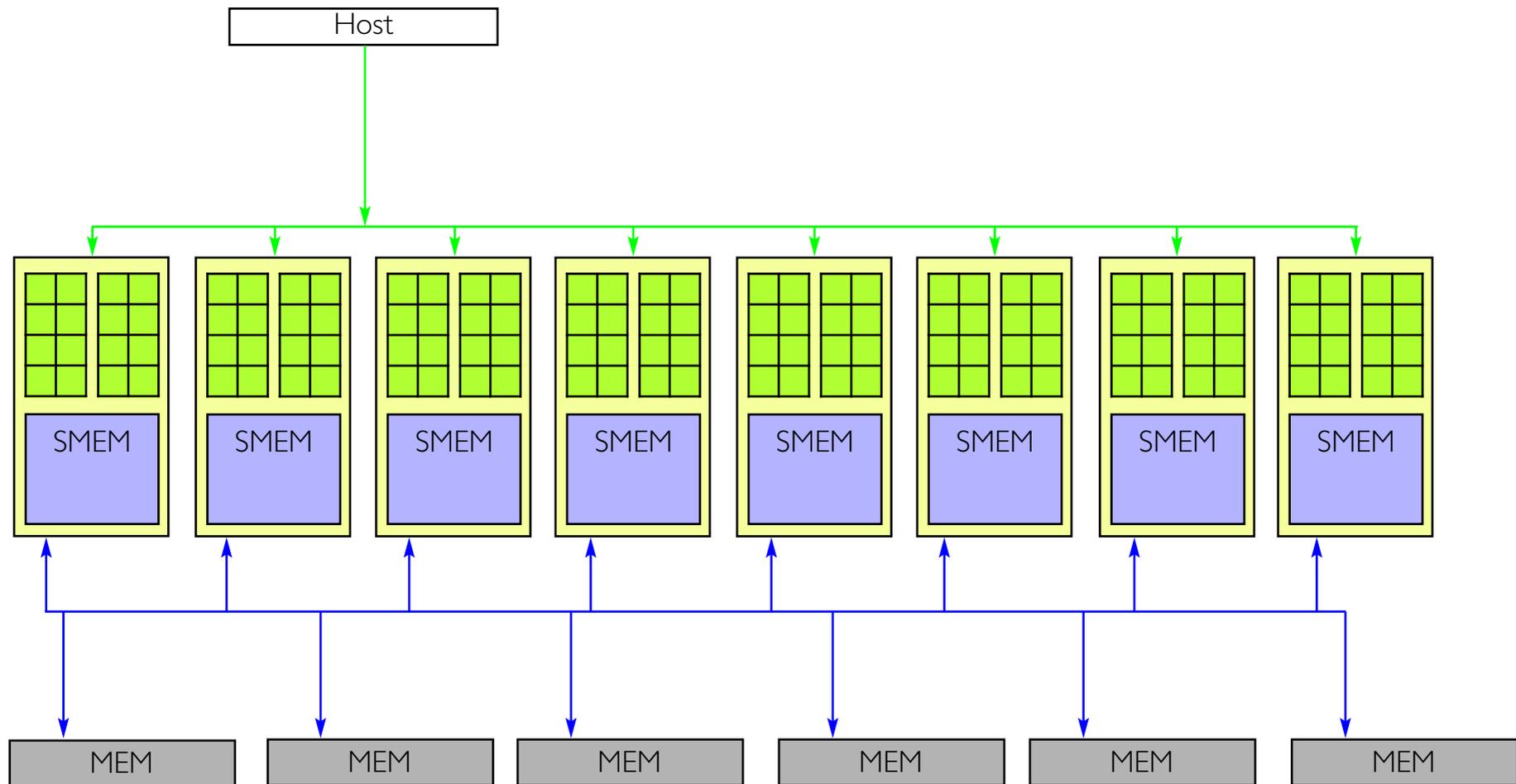
- Orthogonality of the eigenvectors:

$$\|\tilde{\mathbf{U}}^T \tilde{\mathbf{U}} - \mathbf{I}\| = \mathcal{O}(n \epsilon)$$

Data-Parallel Coprocessors



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The MRRR Algorithm

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- Relative distance:

$$\text{reldist}(\lambda_i, \lambda_j) = \frac{|\lambda_i - \lambda_j|}{|\lambda_i|}$$

The MRRR Algorithm

- Matrix shifts:

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- Matrix shifts using Relatively Robust Representations (RRR's):

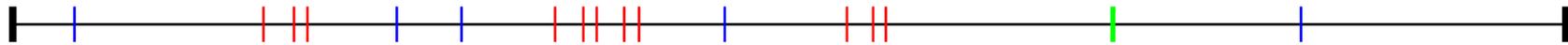
$$\hat{\mathbf{L}}\hat{\mathbf{D}}\hat{\mathbf{L}}^T = \mathbf{L}\mathbf{D}\mathbf{L}^T - \sigma \mathbf{I}$$

The MRRR Algorithm

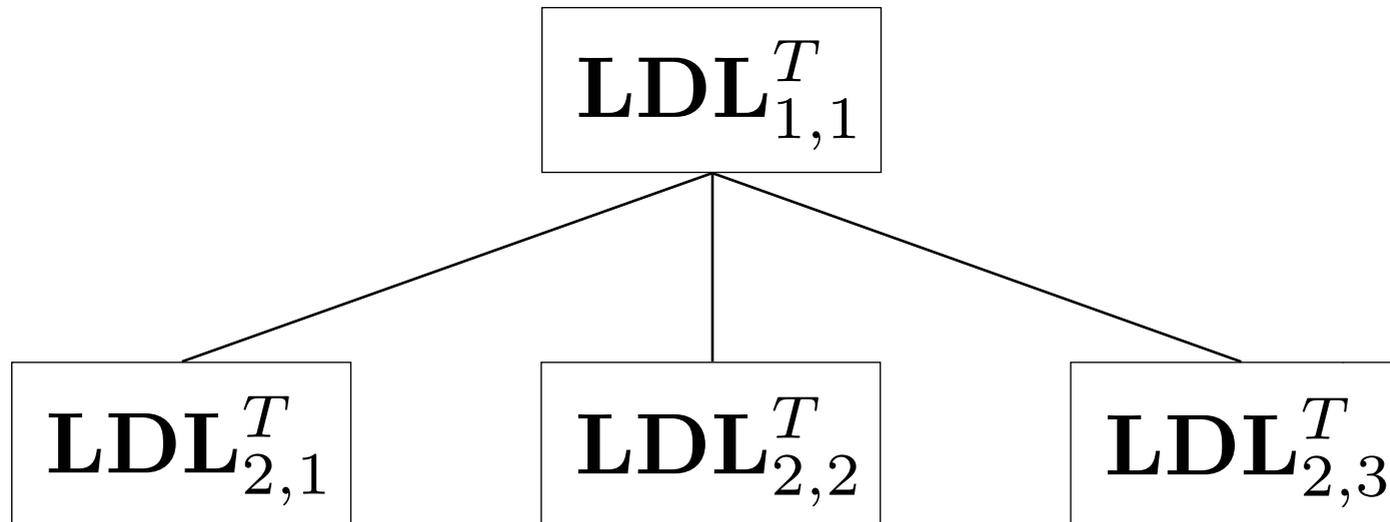
$$\mathbf{LDL}_{1,1}^T$$

The MRRR Algorithm

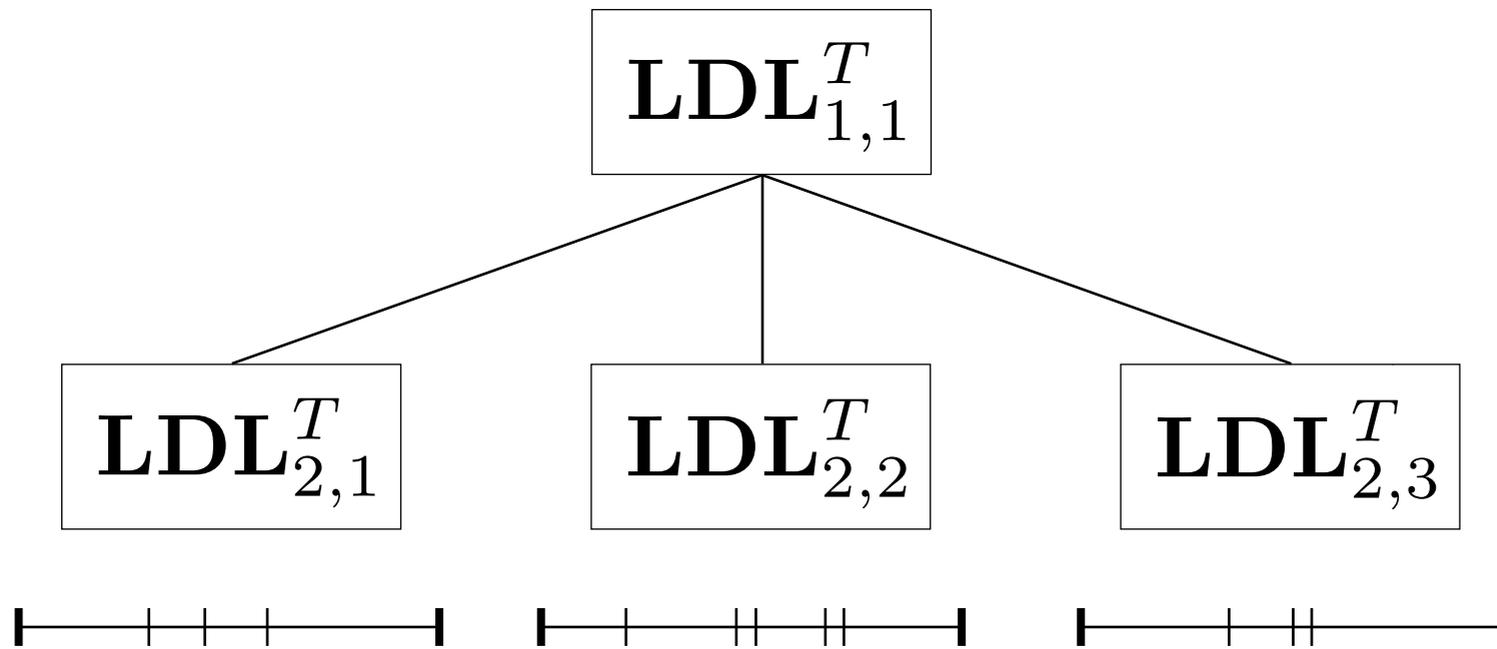
$$\mathbf{LDL}_{1,1}^T$$



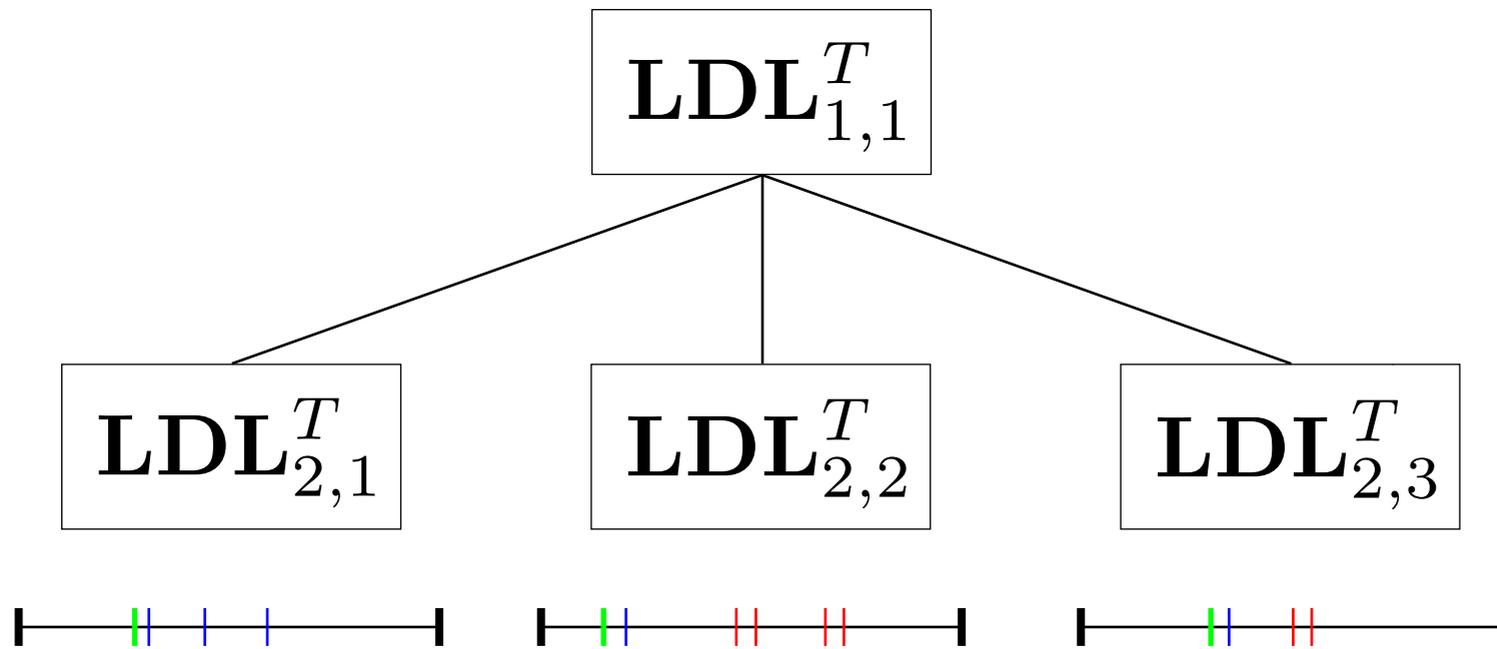
The MRRR Algorithm



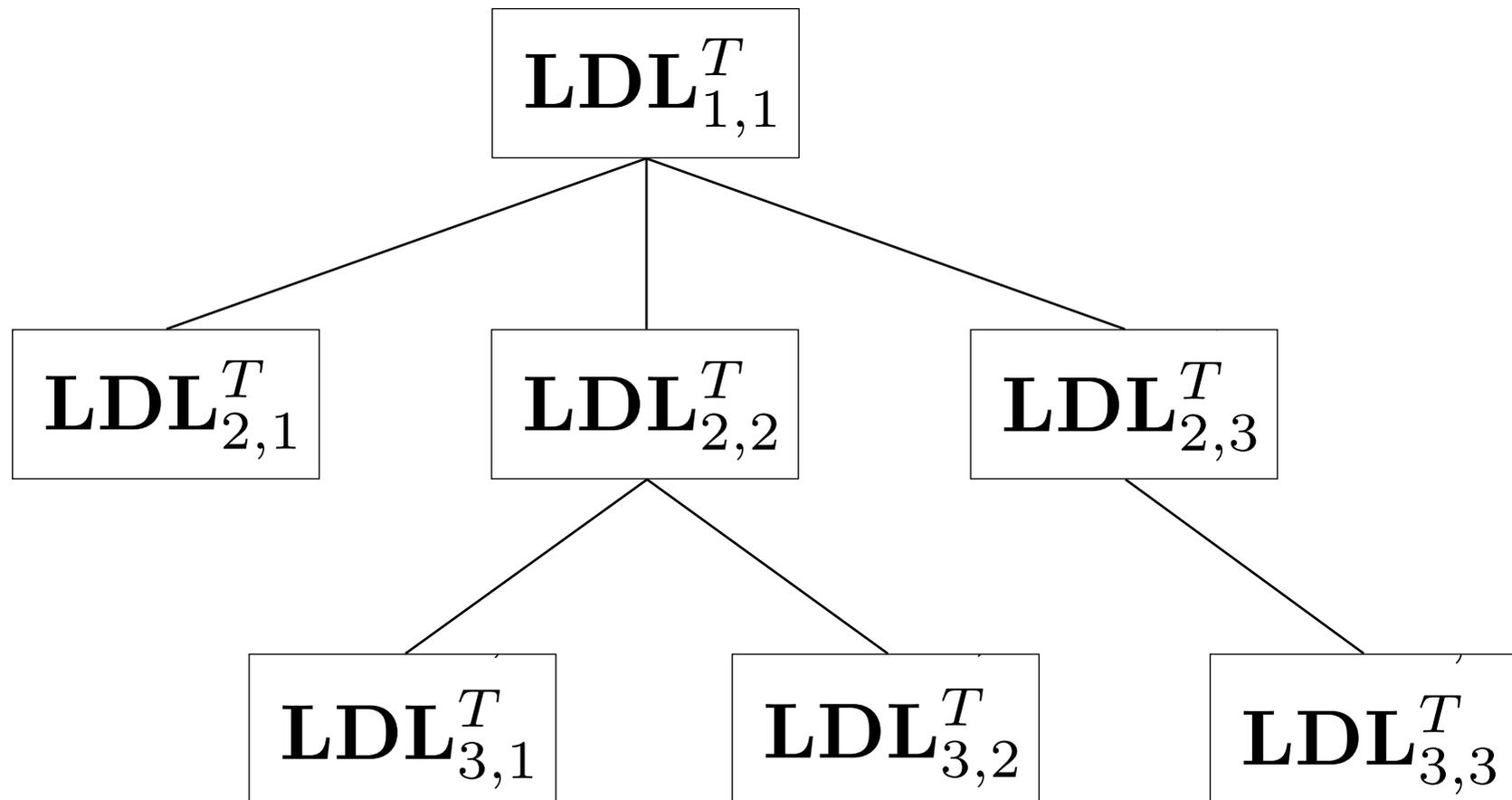
The MRRR Algorithm



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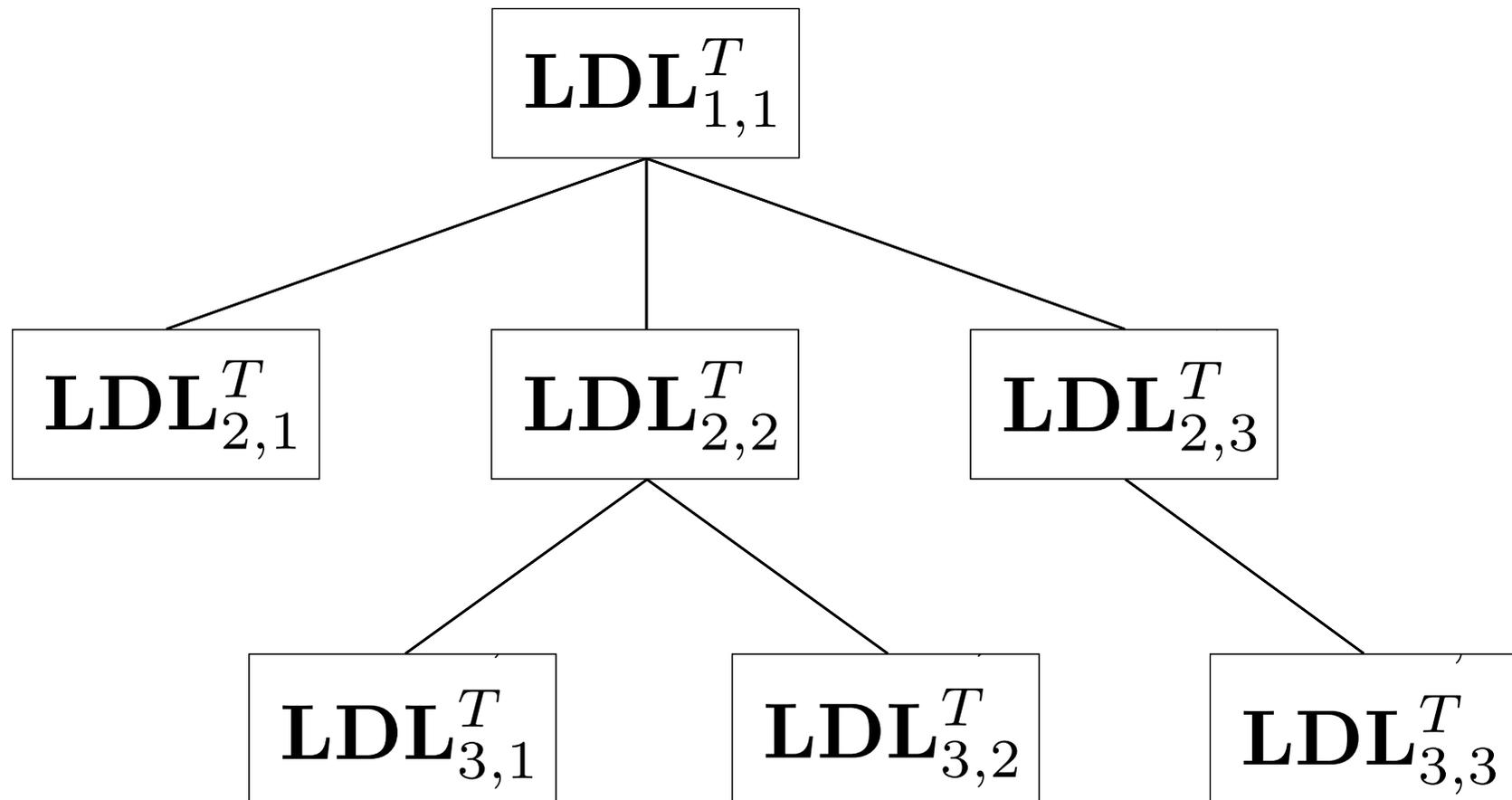
The MRRR Algorithm

For each node (l,m) of the representation tree:

1. Classify eigenvalues as singletons or clustered.
2. Compute eigenpairs for singletons.
3. Compute a shifted matrix and create a new tree node $(l+1,m)$ for every cluster.

Parallelizing the MRRR Algorithm

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Parallelizing the MRRR Algorithm

For each node (l,m) of the representation tree:

1. *For each* eigenvalue, classify it as singletons or part of a cluster.
2. *For each* singleton, compute the eigenpair.
3. *For each* cluster, compute a shifted matrix and create a representation tree node $(l+1,m)$.

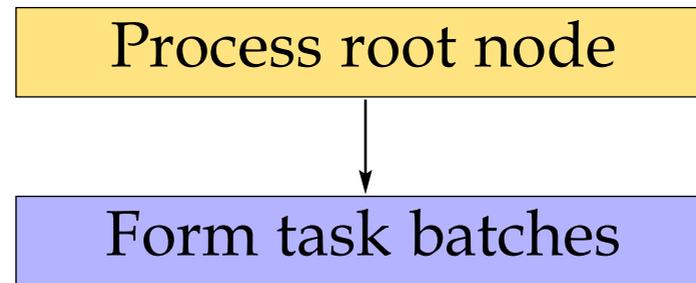
Parallelizing the MRRR Algorithm

- **Task parallelism:** Representation tree allows to process nodes on the same level or in different subtrees independently.
- **Data parallelism:** Computations per cluster are data-parallel.

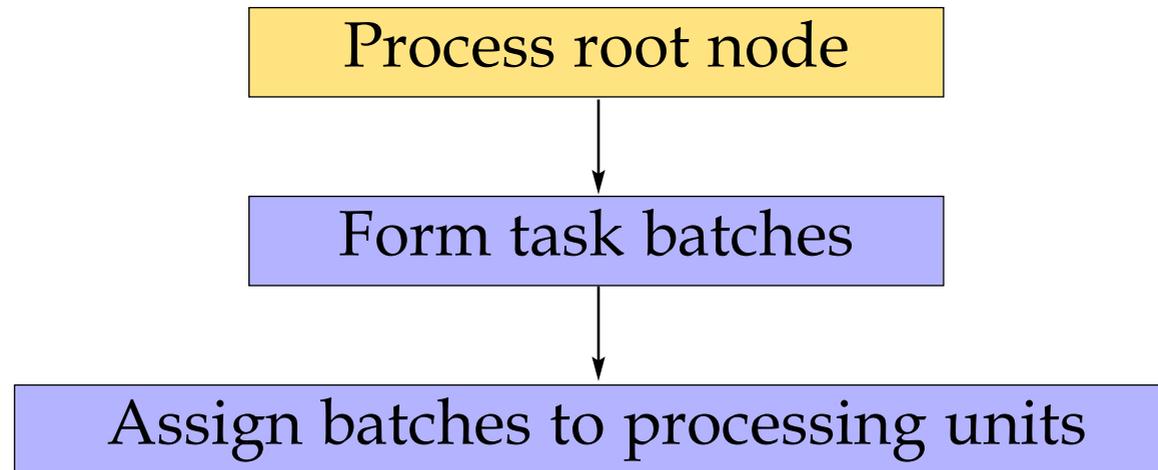
Implementation

Process root node

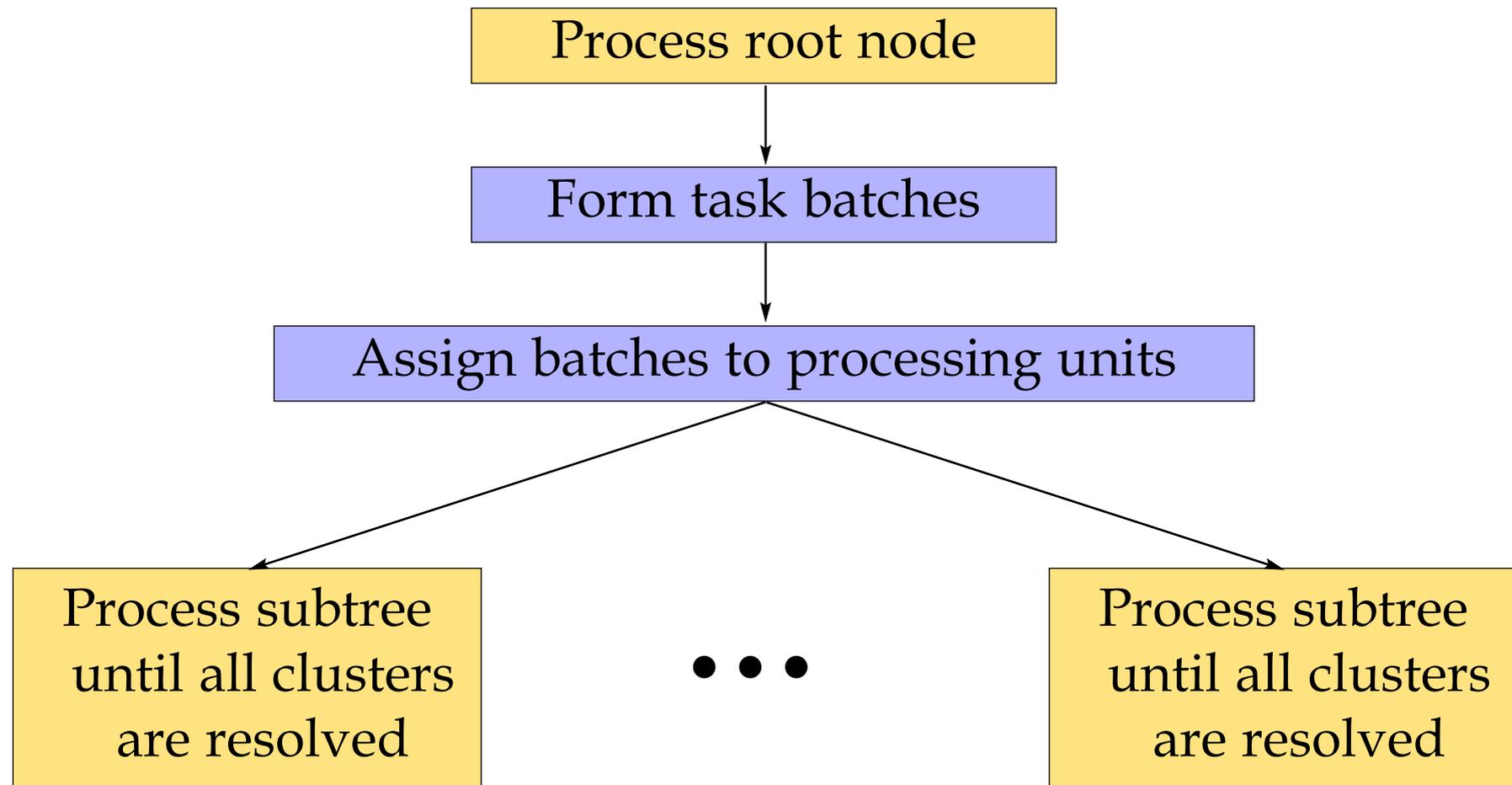
Implementation



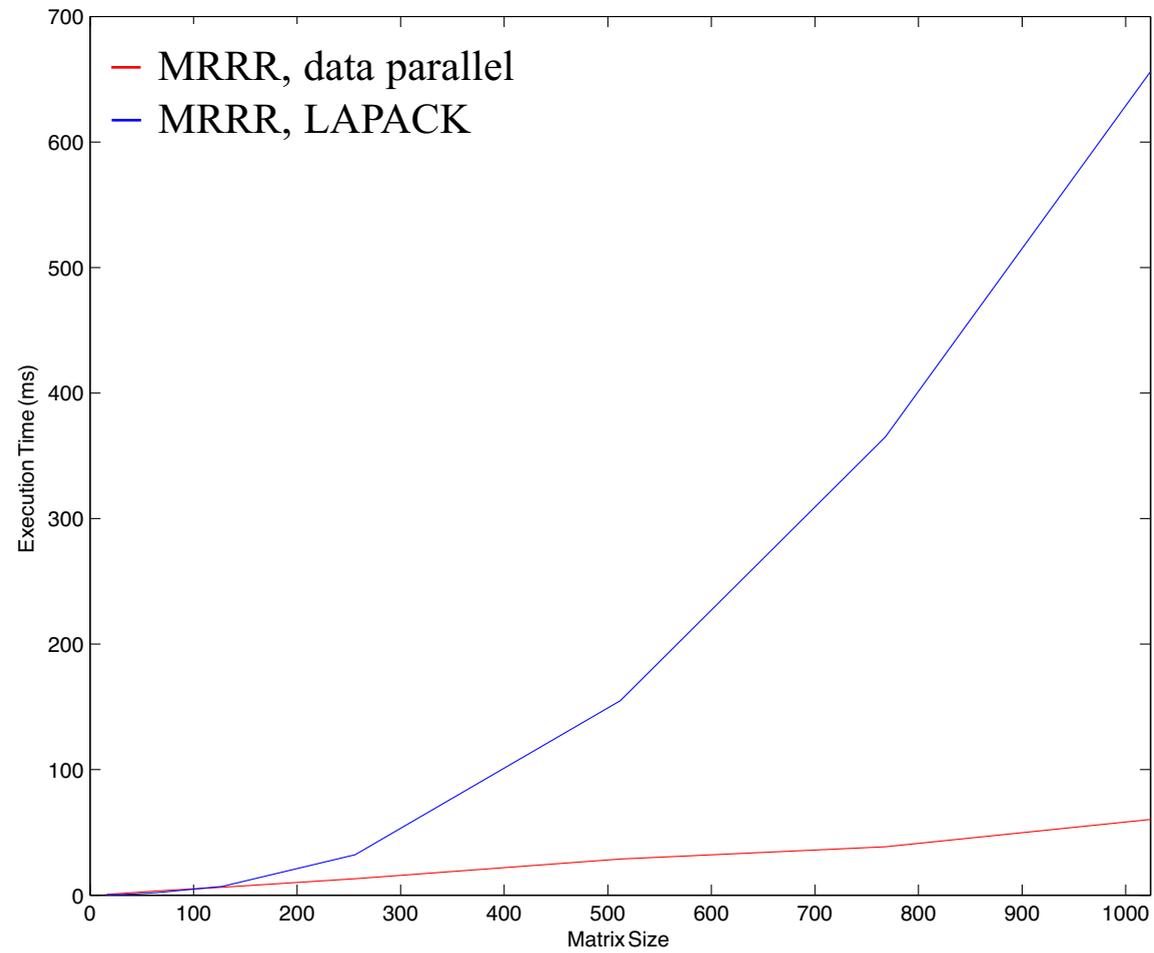
Implementation



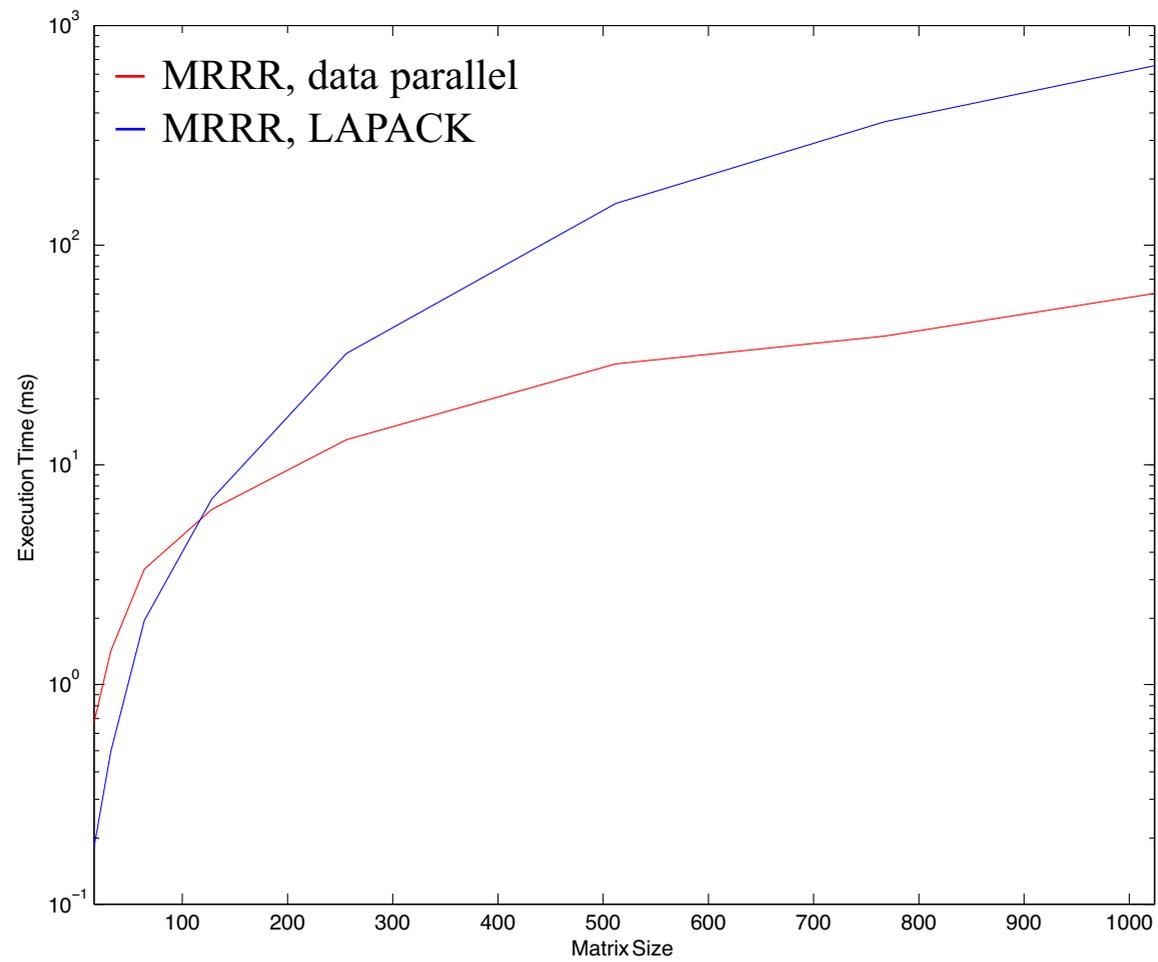
Implementation



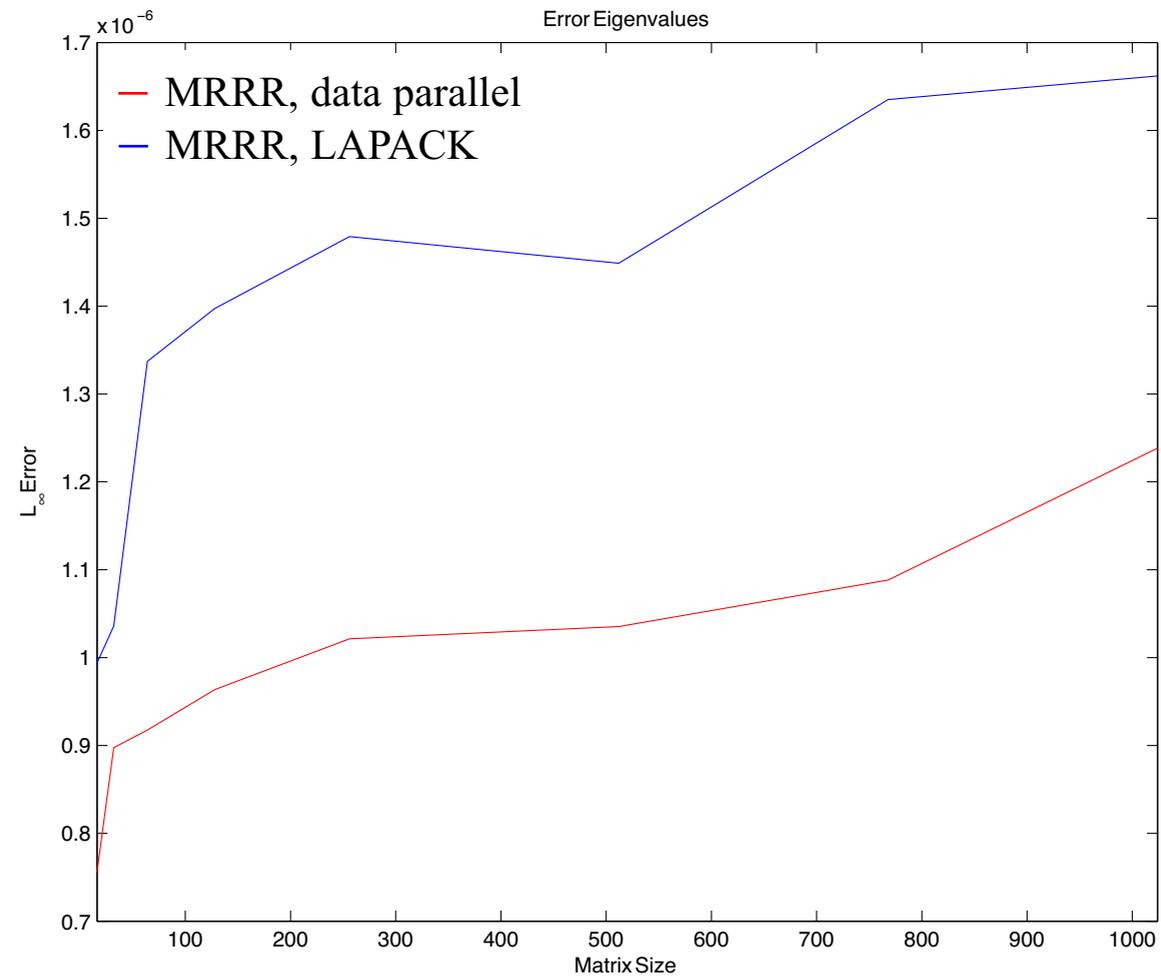
Results



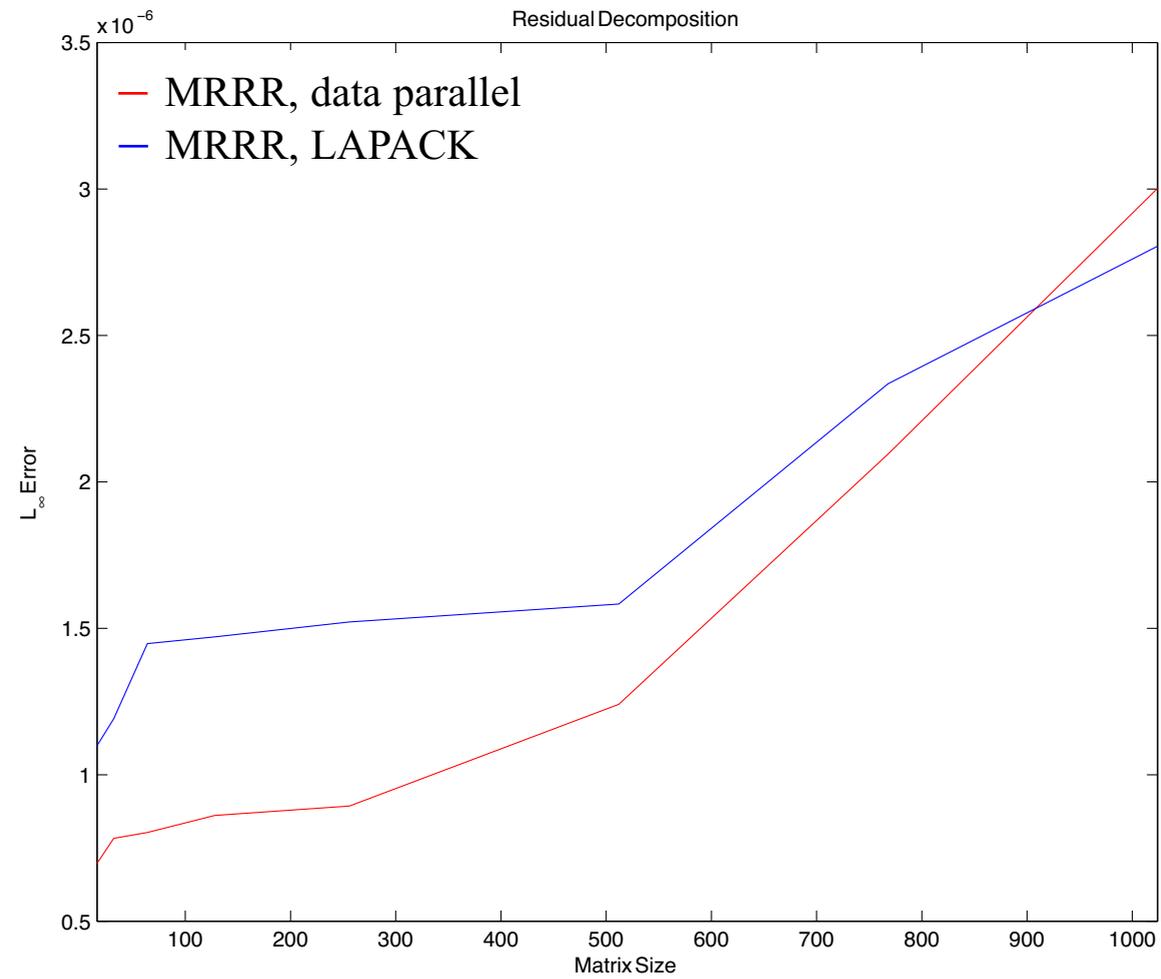
Results



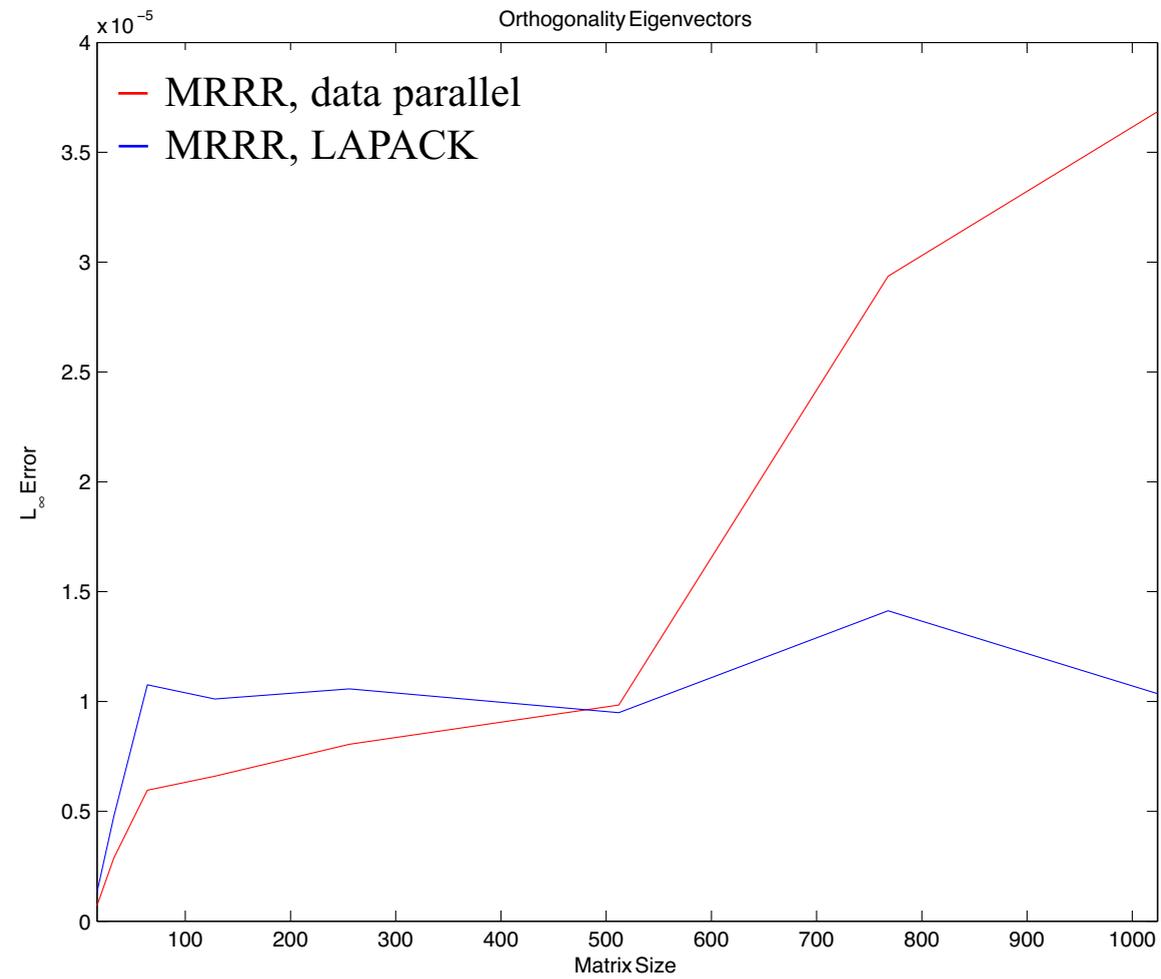
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Conclusion

- MRRR algorithm can be mapped efficiently onto data parallel coprocessors.
 - Representation tree provides task parallelism.
 - Computations for each tree node provide data parallelism.

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- MRRR algorithm can be mapped efficiently onto data parallel coprocessors.
 - Representation tree provides task parallelism.
 - Computations for each tree node provide data parallelism.
- Significant speedups over single-threaded CPU implementation possible.

Open Problems and Future Work

- Resolve remaining accuracy issues.
- Load balancing between processing units.
 - For nodes on the first representation tree level.
 - Load balancing at every level of the representation tree?
- Load balancing between host and device.
- Port to OpenCL and Larrabee.

More details:

www.dgp.toronto.edu/people/lessig/mrrr/